

No. KUDSIT/112/2021

Technocity, Dated: 13.10.2021

NOTIFICATION

Kerala University of Digital Sciences, Innovation and Technology invite applications from qualified Indian Citizens for appointment to the following posts (on a contract basis) for the School of Electronics of this University. Candidates shall apply online through the Recruitment Portal. The guidelines for the submission of applications are available in the recruitment portal.

DETAILS OF POSTS, QUALIFICATION, EXPERIENCE,SKILLS REQUIRED

Sl. no. 1

Job Code Name of Post Name of Laboratory No. of Vacancies Remuneration Age	: RANS/102021/112 : Research Associate (Neural Systems) : Neural Systems Engineering (SE) : 01 : Rs. 25,000 - (Consolidated) : Should not exceed 30 Years as on the last date for submission of application
Job Description	The successful candidate will be: <ul style="list-style-type: none"> responsible for the development of neural chips as part of the project team able to learn, design, and use python tools become proficient in various kinds of IoT testing develop board-level schematics and designs simulate the system under various conditions will work on large system simulations and real-time implementations
Essential Educational Qualification	M.Tech in Electronics or B.Tech/MSc in Electronics or equivalent from a recognized University/Educational Institution.
Essential Experience	Candidates should have knowledge in Python or Circuit simulation tools for deep learning and neural networks. M.Tech graduates with quality publications or chip/PCB design works will be given preference. This is a research and development position.
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl.No. 2.

Job Code	: RANICH/102021/112
Name of Post	: Research Associate(Neural IC Hardware)
Name of Laboratory	: Neural IC Hardware (SE)
No. of Vacancies	: 01
Remuneration	: Rs. 25,000 /(Consolidated)
Age	: Should not exceed 30 Years as on the last date for submission of application
Job Description	<p>The successful candidate will be:</p> <ul style="list-style-type: none"> • responsible for the development of neural chips as part of the project team • able to design and use Mentor Graphics IC design tools • work closely with a semiconductor foundry • develop board-level schematics and designs • simulate the system under various conditions • will work on large system simulations and real-time implementations
Essential Educational Qualification	M.Tech in Electronics or equivalent /B.Tech/MSc in Electronics or equivalent from a recognized University/Educational Institution.
Essential Experience	<ul style="list-style-type: none"> • Should have knowledge in Mentor IC or Cadence tools for digital/analog circuits. • M.Tech graduates with quality publications or chip design works will be given preference. • This is a research and development position
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl. No. 3.

Job Code	: REICD/102021/112
Name of Post	: Research Engineer
Name of Laboratory	: IC Design (SE)
No. of Vacancies	: 02
Remuneration	: Rs. 45,000/- (Consolidated)
Age	: Should not exceed 40 Years as on the last date for submission of application
	<p>The successful candidate will be:</p> <ul style="list-style-type: none"> • responsible for the development of neural chips as part of the project team • able to design and use Mentor Graphics IC design tools • work closely with a semiconductor foundry



Job Description	<ul style="list-style-type: none"> • develop board-level schematics and designs • become proficient in EMC and high-speed design issue • get familiar with FPGA/ASIC implementations • simulate the system under various conditions • will work on large system simulations and real-time implementations • lead or assist in the supervision of RAs and graduate students
Essential Educational Qualification	Ph.D.or MTech in Electronics or equivalent from a recognized University/Educational Institution.
Essential Experience	<ul style="list-style-type: none"> • Candidates with Ph.D. having knowledge in Software development or Electronics Chip Design are eligible. Candidates with M Tech having 5 years of experience in in Software development or Electronics Chip Design are also eligible. • Candidates with experience in chip fabrication and publications in IEEE Transactions will be given preference. • This is a research and development position.
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl.No. 4.

Job Code Name of Post No. of Vacancies Remuneration Age	: RI/102021/112 : Research Intems : 02 : Rs. 15,000/- (Consolidated) : Should not exceed 30 Years as on the last date for submission of application
Job Description	The successful candidate will be: <ul style="list-style-type: none"> • responsible for the development of drone systems • able to program firmware of the drones • develop open-source libraries in python • develop mobile applications in android • get familiar with deep learning systems
Essential Educational Qualification	M.Tech or MSc or B.Tech Degree from a recognized University/Educational Institution.
Essential Experience	No experience is required.
Nature of Appointment	On contract for 6 months



Sl. No. 5.

Job Code	: RADA102021/112
Name of Post	: Research Associate(Data Analytics)
No. of Vacancies	: 02
Remuneration	: Rs. 25,000/- (Consolidated)
Age	: Should not exceed 35 Years as on the last date for submission of application
Job Description	<p>The successful candidate will be:</p> <ul style="list-style-type: none"> • responsible for the programming of drone systems and control systems • able to integrate co-processor systems and sensor systems • develop new libraries for remotely accessing the drones • be familiar with computer vision and embedded systems • get familiar with deep learning systems
Essential Educational Qualification	M.Tech/B.Tech in Electronics or equivalent or MSc in Data Analytics / Machine Intelligence from a recognized University/Educational Institution.
Essential Experience	Minimum 1-year experience in Sensor electronics/ deep learning or drones This is a research and development position
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl. No. 6.

Job Code	: RAMAT/102021/112
Name of Post	: Research Associate(MATLAB)
No. of Vacancies	: 01
Remuneration	: Rs. 25,000/ (Consolidated)
Age	: Should not exceed 40 Years as on the last date for submission of application
Job Description	<p>The successful candidate will be:</p> <ul style="list-style-type: none"> • familiar in using MATLAB • engage in research in the area of signal processing • be able to work in biomedical image processing
Essential Educational Qualification	M.Tech in signal processing or equivalent from a recognized University/Educational Institution.



Essential Experience	Minimum 1 years experience in image processing and MRI. This is a research and development position.
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl. No. 7.

Job Code Name of Post No. of Vacancies Remuneration Age	: REMAT/102021/112 : Research Engineer(MATLAB) : 01 : Rs. 45,000 /- (Consolidated) : Should not exceed 40 Years as on the last date for submission of application
Job Description	The successful candidate will be: <ul style="list-style-type: none"> • familiar in using MATLAB • engage in research in the area of signal processing • be able to work in biomedical image processing
Essential Educational Qualification	Ph.D or MTech in Signal Processing or equivalent from a recognized University/Educational Institution.
Essential Experience	Candidates with Ph.D in the relevant area are eligible. Candidates with M Tech and 5 years experience in DSP systems and Biomedical Imaging systems or MRI are also eligible This is a research and development position.
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Sl. No. 8.

Job Code Name of Post No. of Vacancies Remuneration Age	: IRC/102021/112 : Industry Relations Coordinator : 01 : Rs. 25,000 /- (Consolidated) : Should not exceed 30 Years as on the last date for submission of application
Job Description	The successful candidate will be: <ul style="list-style-type: none"> • liaison with industry for providing internships to the M.Tech students • work closely with the department chair and faculty members to manage school documents and meetings • develop marketing content for advertising the school activities • be able to create web pages and maintain the content regularly



	<ul style="list-style-type: none"> actively organize events and career fairs for the M.Tech students in electronics
Essential Educational Qualification	B.Tech or equivalent in any branch from a recognized University/Educational Institution.
Essential Experience	At least 1 year of relevant experience Excellent verbal communication skills and technical writing skills are required. This job is focused on administrative assistance, marketing support, and industry Liaison.
Nature of Appointment	On contract (initially for 6 months which is likely to be extended based on performance/requirement.)

Note:

- Age relaxation for submission of applications: 5 years for candidates belonging to SC/ST and 3 years for OBC candidates.
- In the case of an Ex-serviceman or Ex-general Reserve Engineer Force person or a disembodied Territorial Army person, in reckoning the age for his eligibility for submission of application, can exclude the period of his service in the defense forces or the General Reserve Engineer Force or the Territorial Army, as the case may be, and the period of unemployment on discharge up to a maximum of five years provided he has not completed the age of 50 years.
- Age relaxation up to 10 years to orthopedically disabled candidates subject to the condition that in no case the upper age limit shall exceed 50 years.

Mode of Selection:

- The selection will be based on an Objective Test/Descriptive Test and/ or interview conducted by the University or an outside agency depending on the number of applicants.
- In case a test is conducted, the syllabus of the Test will be intimated through Admission Ticket and the medium of the test will be English.
- The minimum marks for being included in the Short List of candidates to be called for the interview will be 40% in the Objective/Descriptive Test. The number of candidates to be included in the Short List of candidates to be called for the interview will be decided by the University.
- The final selection will be based on the decision of the Interview Board.

Facilities for Persons with Disabilities: (In case a Test is conducted)

- Candidates with benchmark disabilities whose writing speed is affected can avail of the services of a scribe. Such candidates should apply for the same separately. They, whether availing the facility of a scribe or not, will be allowed compensatory time of 20 minutes and/or part thereof for every hour of the examination.
- The use of scribe will be governed by the guidelines prescribed by the University and candidates will have to produce a medical certificate and declaration at the time of attending the test.



Venue of Test/Interview

The test/ Interview will be conducted at Thiruvananthapuram only.

General Instructions

- Scanned copy of documents to prove age, qualifications, experience , community /Non-Creamy Layer status, economically weaker section status, etc. should be uploaded along with online application and the original documents must be produced as and when called for, failure of which will entail cancellation of candidature. Candidates who belong to differently-abled categories and claim age relaxation will have to upload copy of certificate from the medical board as per G.O (P) No.39/2012/SWD dated 30/06/2012 and produce the same as and when called for.
- Candidates, who are in the service of the Government of India or any of the State Governments, etc. while applying, should obtain a No Objection Certificate from their Head of Office or Department and keep it with them. If candidates fail to produce the No Objection Certificate as and when called for, his/her candidature will stand canceled.
- Candidates should ensure that the particulars furnished by him/her are correct in all respects. **Anything not specifically claimed in the application against the appropriate field will not be considered at a later stage.** In case it is detected at any stage of the recruitment that a candidate does not fulfill the eligibility norms and /or that he/she furnished any incorrect/false information or has suppressed any material fact, his/her candidature will stand canceled. If suppression of material facts or furnishing of any incorrect/false information is detected even after appointment, his/her service is liable to be terminated.
- Applications without fulfilling the the eligibility and other norms mentioned in this Notification as well as the instructions published in the Recruitment Portal of the University will be summarily rejected. **Applications submitted without accompanying scanned copies of the required documents to prove date of birth, qualification , experience etc will be summarily rejected.** There will be no further chance for the submission of the same
- Canvassing in any form will entail the cancellation of candidature.

How to Apply:

- Applications shall be submitted online latest on **31.10.2021**. The application form is available at <https://www.duk.ac.in/careers>.
- The application fee for the posts will be as follows;

Sl No.	Name of Post	Fee
1	Research Engineer	Rs. 300/-
2	Research Associates	Rs. 250/-
3	Industry Relations Coordinator	Rs. 250/-
4	Research Interns	NIL

(Candidates belonging to SC/ST and Differently-Abled categories are exempted from payment of application fee). Instructions regarding the mode of payment are available in the Recruitment portal.



- iii. The applications received after the last date fixed or the incomplete applications or the applications without filling the mandatory fields will be summarily rejected.
- iv. No interim queries after the submission of the application will be entertained. The candidates will be intimated the schedule of the Test/ Interview through their registered email only.
- v. The candidates are advised to check their registered e-mails regularly after the last date fixed for the submission of the application.
- vi. The University has the right to cancel the advertisement or entire selection process at any stage without assigning reason thereof and the decision of the University shall be final.

Sd/-
REGISTRAR

