

No.KUDSIT/112/AD AIII/2026

Dated:19.02.2026

WALK-IN INTERVIEW

Kerala University of Digital Sciences, Innovation and Technology invites applications from qualified Indian Citizens for appointment to the following positions (on a contract basis) under the Graphene Aurora project of the University.

The walk-in-interview shall be conducted from 10:00 AM to 12:00 PM on 26.02.2026 at the Digital University, Campus, Mangalapuram, Thiruvananthapuram.

DETAILS OF POST, QUALIFICATION, EXPERIENCE ETC. REQUIRE.

I. Project Scientist-I/II/II - up to 8 Nos

1. Project Scientist-I

Job Code	PS-I/22026/112
Name of post	Project Scientist-I
Remuneration	Rs.56,000/- per month+HRA
Age	Applicants should not exceed 40 years of age as on the date of the interview.
Essential Educational Qualification	Doctoral Degree in Science / Engineering / Technology OR Master's Degree in Engineering / Technology from a recognized University or equivalent.
Area of Expertise Required	<p>(1) VLSI / Electronic Design</p> <ul style="list-style-type: none"> • Analog and Mixed Signal IC Design • Digital VLSI Design and Verification • FPGA Prototyping • Semiconductor Device Modeling • EDA Tools (Cadence / Synopsys / Mentor Graphics) <p>(2) 2D Materials / Graphene</p> <ul style="list-style-type: none"> • Synthesis and Characterization of 2D Materials • CVD / Exfoliation Techniques • Nanofabrication and Device Integration • Electrical and Optical Characterization • Applications in Nanoelectronics / Sensors



Nature of Appointment	On contract basis for one year. However, the term may be extended further based on the performance and project requirements.
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2. Project Scientist-II

Job Code	PS-II/22026/112
Name of post	Project Scientist-II
Remuneration	Rs.67,000/- per month + HRA
Age	Applicants should not exceed 40 years of age as on the date of the interview.
Essential Educational Qualification	Doctoral Degree in Science / Engineering / Technology OR Master's Degree in Engineering / Technology from a recognized University or equivalent.
Essential Experience	Three years' experience in Research & Development in Industrial / Academic / S&T Organizations.
Area of Expertise Required	<p>(1) VLSI / Electronic Design</p> <ul style="list-style-type: none"> • Analog and Mixed Signal IC Design • Digital VLSI Design and Verification • FPGA Prototyping • Semiconductor Device Modeling • EDA Tools (Cadence / Synopsys / Mentor Graphics) <p>(2) 2D Materials / Graphene</p> <ul style="list-style-type: none"> • Synthesis and Characterization of 2D Materials • CVD / Exfoliation Techniques • Nanofabrication and Device Integration • Electrical and Optical Characterization • Applications in Nanoelectronics / Sensors
Nature of Appointment	On contract basis for one year. However, the term may be extended further based on the performance and project requirements.

3. Project Scientist-III

Job Code	PS-III/22026/112
Name of post	Project Scientist-III
Remuneration	Rs.78,000/- per month + HRA
Age	Applicants should not exceed 45 years of age as on the date of the interview.



Essential Educational Qualification	Doctoral Degree in Science / Engineering / Technology OR Master's Degree in Engineering / Technology from a recognized University or equivalent.
Essential Experience	Seven years experience in Research & Development in Industrial / Academic / S&T Organizations.
Area of Expertise Required	<p>(1) VLSI / Electronic Design</p> <ul style="list-style-type: none"> • Analog and Mixed Signal IC Design • Digital VLSI Design and Verification • FPGA Prototyping • Semiconductor Device Modeling • EDA Tools (Cadence / Synopsys / Mentor Graphics) <p>(2) 2D Materials / Graphene</p> <ul style="list-style-type: none"> • Synthesis and Characterization of 2D Materials • CVD / Exfoliation Techniques • Nanofabrication and Device Integration • Electrical and Optical Characterization • Applications in Nanoelectronics / Sensors
Nature of Appointment	On contract basis for one year. However, the term may be extended further based on the performance and project requirements.

II. Project Associate-I/II- 2 Nos

1. Project Associate-I

Job Code	PA-I/22026/112
Name of post	Project Associate-I
Remuneration	Rs.31,000/- + HRA
Age	Applicants should not exceed 40 years of age as on the date of the interview.
Essential Educational Qualification	Master's Degree in Natural / Agricultural Sciences / MVSc OR Bachelor's Degree in Engineering / Technology / Medicine from a recognized University or equivalent.
Area of Expertise Required	<p>(1) VLSI / Electronic Design</p> <ul style="list-style-type: none"> • Analog and Mixed Signal IC Design • Digital VLSI Design and Verification • FPGA Prototyping • Semiconductor Device Modeling • EDA Tools (Cadence / Synopsys / Mentor Graphics)



	<p>(2) 2D Materials / Graphene</p> <ul style="list-style-type: none"> • Synthesis and Characterization of 2D Materials • CVD / Exfoliation Techniques • Nanofabrication and Device Integration • Electrical and Optical Characterization • Applications in Nanoelectronics / Sensors
Nature of Appointment	On contract basis for one year. However, the term may be extended further based on the performance and project requirements.

2. Project Associate-II

Job Code	PA-II/22026/112
Name of post	Project Associate-II
Remuneration	Rs.35,000/- + HRA
Age	Applicants should not exceed 40 years of age as on the date of the interview.
Essential Educational Qualification	Master's Degree in Natural / Agricultural Sciences / MVSc OR Bachelor's Degree in Engineering / Technology / Medicine from a recognized University or equivalent.
Essential Experience	Two years experience in Research & Development in Industrial / Academic / S&T Organizations.
Area of Expertise Required	<p>(1) VLSI / Electronic Design</p> <ul style="list-style-type: none"> • Analog and Mixed Signal IC Design • Digital VLSI Design and Verification • FPGA Prototyping • Semiconductor Device Modeling • EDA Tools (Cadence / Synopsys / Mentor Graphics) <p>(2) 2D Materials / Graphene</p> <ul style="list-style-type: none"> • Synthesis and Characterization of 2D Materials • CVD / Exfoliation Techniques • Nanofabrication and Device Integration • Electrical and Optical Characterization • Applications in Nanoelectronics / Sensors
Nature of Appointment	On contract basis for one year. However, the term may be extended further based on the performance and project requirements.



Age relaxation for submission of application: 5 years for candidates belonging to SC/ ST and 3 years for OBC candidates.

Mode of Selection: The selection will be based on a test and Interview

Venue of Test/Interview: The test /Interview will be conducted in Thiruvananthapuram district only.

General Instructions

1. Candidates must bring all original certificates along with a set of self-attested photocopies (Age, Date of Birth, Qualification, Experience, etc.) for verification at the time of the interview.
2. Candidates who fail to produce the original certificates for verification will not be allowed to attend the interview, leading to forfeiture of candidature.
3. Candidates, who are in the service of the Government of India or any of the State Governments, etc. while applying, should obtain a No Objection Certificate from their Head of Office or Department and keep it with them. If candidates fail to produce the No Objection Certificate as and when called for, his/her candidature will stand cancelled.
4. Candidates should ensure that the particulars furnished by him/her are correct in all respects. Anything not specifically claimed in the application against the appropriate field will not be considered at a later stage. No deviation whatsoever from the filled in data will be entertained. In case it is detected at any stage of the recruitment that a candidate does not fulfill the eligibility norms and /or that he/she furnished any incorrect/false information or has suppressed any material fact, his/her candidature will stand cancelled. If suppression of material facts or furnishing of any incorrect/false information is detected even after appointment, his/her service is liable to be terminated.
5. In case any discrepancy / ambiguity in the process of selection, the decision of the University shall be final and binding.
6. Consequent up on adoption of self- certification provisions, the University shall process the applications entirely on the basis of the information/documents provided with the application. The selection / appointment will be provisional and subject to physical verification of the documents.
7. Canvassing in any form will entail the cancellation of candidature.
8. HRA will be provided as per Government of India norms, as applicable. No DA & CCA will be admissible.

How to Apply

Interested candidates shall submit the application to graphene@duk.ac.in along with detailed CV, copies of certificates, and proof of experience. You are required to bring the originals during the written test / interview at 10:00 AM to 12:00 PM on 26.02.2026 at the Digital University, Campus, Mangalapuram, Thiruvananthapuram.

Dean HR



* This is a computer system (Digital File) generated letter. Hence there is no need for a physical signature.

