

No. KUDSIT/564/CSES/2025

Dated: 24.02.2026

WALK-IN INTERVIEW

The Kerala University of Digital Sciences, Innovation and Technology invites applications from qualified Indian Citizens for appointment to the following positions on a contract basis under the School of Electronic Systems & Automation (SoESA).

The walk-in-interview shall be conducted from **10:00 AM to 12:00 PM on 02.03.2026 at the Digital University, Campus, Mangalapuram, Thiruvananthapuram.**

DETAILS OF POST, QUALIFICATION, EXPERIENCE ETC. REQUIRED

I. Guest Faculty - Analog/Digital VLSI Design

Job Code	GF-AD/022026/426
Name of post	Guest Faculty-Analog/Digital VLSI Design
No. of Vacancies	One
Remuneration	Rs.50,000 per month consolidated
Age	Should not exceed 50 years as of the last date for submission of application.
Essential Educational Qualification	B.Tech and M.Tech in Electronics/ Electronics and Communication/ Instrumentation/ Electrical Engineering/ VLSI/ VLSI Design/ Microelectronics/ Microelectronics & VLSI/ Embedded systems/ VLSI & Embedded Systems/ Semiconductor Technology or closely related areas.
Desirable Qualification	Ph.D in VLSI design/Microelectronic Circuits or allied areas.
Essential Experience	Minimum 1 year of teaching/ research/ industry experience in VLSI-related topics/areas, including digital/analog/mixed signal circuits. Expected to have strong foundations in HDL and circuits topics.
Job Requirement	This position aims to supplement the DUK's faculty efforts and enhance instructional delivery by engaging professionals to mentor the students in OBE (Outcome- Based Education courses) at the 400 and 500 Level.
Nature of Appointment	On Contract (One semester)



II. Guest Faculty - Semiconductor Materials and Devices

Job Code	GF-SMD/22026/426
Name of post	Guest Faculty-Semiconductor Materials and Devices
No. of Vacancies	One
Remuneration	Rs.50,000 per month consolidated
Age	Should not exceed 50 years as of the last date for submission of application.
Essential Educational Qualification	B.Tech and M.Tech in Electronics/ Electronics and Communication/ Instrumentation/ Electrical Engineering/ VLSI/ VLSI Design/ Microelectronics/ Nanoelectronics/ Microelectronics & VLSI/ Embedded systems/ VLSI & Embedded Systems/ Semiconductor Technology/ Quantum Technologies or closely related areas.
Desirable Qualification	Ph.D in Microelectronics/VLSI/Semiconductor Device Physics/ Nanoelectronics/ Quantum Technologies or allied areas.
Essential Experience	Minimum 1 year of teaching/ research/ industry experience in semiconductor device-related areas/topics, including solid state devices, electronic devices and circuits, nanoelectronics, quantum theory and materials, etc.
Job Requirement	This position aims to supplement the DUK's faculty efforts and enhance instructional delivery by engaging professionals to mentor the students in OBE (Outcome- Based Education courses) at the 400 and 500 Level
Nature of Appointment	On Contract (One semester)

Age relaxation for submission of application: 5 years for candidates belonging to SC/ ST and 3 years for OBC candidates.

Mode of Selection: The selection will be based on Interview.

Venue of Interview: The Interview will be conducted in Thiruvananthapuram district only.

Job Location: For all posts will be at Thiruvananthapuram

General Instructions

1. Candidates must bring all original certificates along with a set of self-attested photocopies (Age, Date of Birth, Qualification, Experience, etc.) for verification at the time of the interview.
2. Candidates who fail to produce the original certificates for verification will not be allowed to attend the interview, leading to forfeiture of candidature.
3. Candidates, who are in the service of the Government of India or any of the State Governments, etc. while applying, should obtain a No Objection Certificate from their Head of Office or Department and keep it with them. If candidates fail to produce the No Objection Certificate as and when called for, his/her candidature will stand cancelled.
4. Candidates should ensure that the particulars furnished by him/her are correct in all respects. Anything not specifically claimed in the application against the appropriate field will not be considered at a later stage. No deviation



whatsoever from the filled in data will be entertained. In case it is detected at any stage of the recruitment that a candidate does not fulfill the eligibility norms and /or that he/she furnished any incorrect/false information or has suppressed any material fact, his/her candidature will stand cancelled. If suppression of material facts or furnishing of any incorrect/false information is detected even after appointment, his/her service is liable to be terminated.

5. In case any discrepancy / ambiguity in the process of selection, the decision of the University shall be final and binding.
6. Consequent up on adoption of self- certification provisions, the University shall process the applications entirely on the basis of the information/documents provided with the application. The selection / appointment will be provisional and subject to physical verification of the documents.
7. Canvassing in any form will entail the cancellation of candidature.

Dean HR

* This is a computer system (Digital File) generated letter. Hence there is no need for a physical signature.

